

| Parameter | Rating | Units |
| :--- | :---: | :---: |
| Blocking Voltage | 100 | $\mathrm{~V}_{P}$ |
| Load Current | 300 | $\mathrm{~mA}_{D C}$ |
| On-Resistance (max) | 4 | $\Omega$ |

## Features

- Operating Temperature Range: $-40^{\circ} \mathrm{C}$ to $+110^{\circ} \mathrm{C}$
- $1500 \mathrm{~V}_{\text {rms }}$ Input/Output Isolation
- Small 4-Pin SOP Package
- Low Drive Power Requirements
- High Reliability
- Arc-Free With No Snubbing Circuits
- No EMI/RFI Generation
- Wave Solderable
- Tape \& Reel Version Available


## Applications

- Instrumentation
- Multiplexers
- Data Acquisition
- Electronic Switching
- I/O Subsystems
- Meters (Watt-Hour, Water, Gas)
- Medical Equipment—Patient/Equipment Isolation
- Security Systems
- Aerospace
- Industrial Controls
- Reed Relay Replacement


## Description

CPC1004N is a miniature, low-voltage, low on-resistance, normally-open (1-Form-A) DC solid state relay in a 4 -pin SOP package. The relay uses optically coupled MOSFET technology to provide $1500 \mathrm{~V}_{\mathrm{rms}}$ of input to output isolation. The efficient MOSFET switch and photovoltaic die use IXYS Integrated Circuits Division's patented OptoMOS architecture while the optically coupled output is controlled by a highly efficient infrared LED.

The CPC1004N uses IXYS Integrated Circuits Division's state of the art double-molded vertical construction packaging to produce one of the world's smallest relays. The CPC1004N is ideal for replacing larger, less-reliable reed and electromechanical relays.

## Approvals

- UL Recognized Component: File E76270
- CSA Certified Component: Certificate 1172007
- EN/IEC 60950-1 Certified Component:

Certificate B 131282667003

## Ordering Information

| Part \# | Description |
| :--- | :--- |
| CPC1004N | 4-Pin SOP (100/tube) |
| CPC1004NTR | 4-Pin SOP (2000/reel) |

Pin Configuration


## Switching Characteristics of Normally-Open Devices



Absolute Maximum Ratings @ $25^{\circ} \mathrm{C}$

| Parameter | Ratings | Units |
| :--- | :---: | :---: |
| Blocking Voltage | 100 | $\mathrm{~V}_{\mathrm{p}}$ |
| Reverse Input Voltage | 5 | V |
| Input Control Current <br> Peak (10ms) | 50 | mA |
| Input Power Dissipation | 1 | A |
| Total Power Dissipation ${ }^{1}$ | 70 | mW |
| Capacitance Input to Output | 1 | mW |
| Isolation Voltage, Input to Output | 1500 | pF |
| Operational Temperature | -40 to +110 | $\mathrm{~V}_{\mathrm{rms}}$ |
| Storage Temperature | -40 to +125 | ${ }^{\circ} \mathrm{C}$ |

Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

Typical values are characteristic of the device at $+25^{\circ} \mathrm{C}$, and are the result of engineering evaluations. They are provided for information purposes only, and are not part of the manufacturing testing requirements.

Electrical Characteristics @ $25^{\circ} \mathrm{C}$ (Unless Otherwise Specified)

| Parameter | Conditions | Symbol | Min | Typ | Max | Units |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Output Characteristics |  |  |  |  |  |  |
| Load Current <br> Continuous ${ }^{1}$ <br> Continuous <br> Peak |  | $\mathrm{I}_{\mathrm{L}}$ |  |  |  | $m A_{D C}$ |
|  | - |  | - | - | 300 |  |
|  | $\mathrm{T}=110^{\circ} \mathrm{C}, \mathrm{I}_{\mathrm{F}}=10 \mathrm{~mA}$ |  | - | - | 100 |  |
|  | $\mathrm{t}=10 \mathrm{~ms}$ | $\mathrm{I}_{\text {LPK }}$ | - | - | 500 |  |
| On-Resistance ${ }^{2}$ | $\mathrm{I}_{\mathrm{L}}=300 \mathrm{~mA}$ | $\mathrm{R}_{\mathrm{ON}}$ | - | - | 4 | $\Omega$ |
| Off-State Leakage Current | $\mathrm{V}_{\mathrm{L}}=100 \mathrm{~V}_{\mathrm{P}}$ | $\mathrm{I}_{\text {LEAK }}$ | - | - | 1 | $\mu \mathrm{A}$ |
| Switching Speeds <br> Turn-On <br> Turn-Off | $\mathrm{I}_{\mathrm{F}}=5 \mathrm{~mA}, \mathrm{~V}_{\mathrm{L}}=10 \mathrm{~V}$ |  |  |  |  | ms |
|  |  | $\mathrm{t}_{\text {on }}$ | - | - | 3 |  |
|  |  | $\mathrm{t}_{\text {off }}$ | - | - | 1 |  |
| Output Capacitance | $\mathrm{I}_{\mathrm{F}}=0 \mathrm{~mA}, \mathrm{~V}_{\mathrm{L}}=50 \mathrm{~V}, \mathrm{f}=1 \mathrm{MHz}$ | $\mathrm{C}_{\text {OUT }}$ | - | 25 | - | pF |
| Input Characteristics |  |  |  |  |  |  |
| Input Control Current to Activate | $\mathrm{I}_{\mathrm{L}}=300 \mathrm{~mA}$ | $I_{F}$ | - | 0.9 | 2 | mA |
| Input Control Current to Deactivate | - | $I_{\text {F }}$ | 0.3 | 0.8 | - | mA |
| Input Voltage Drop | $\mathrm{I}_{\mathrm{F}}=5 \mathrm{~mA}$ | $V_{F}$ | 0.9 | 1.2 | 1.4 | V |
| Reverse Input Current | $\mathrm{V}_{\mathrm{R}}=5 \mathrm{~V}$ | $\mathrm{I}_{\text {R }}$ | - | - | 10 | $\mu \mathrm{A}$ |

[^0]PERFORMANCE DATA @ $\mathbf{2 5}^{\circ} \mathrm{C}$ (Unless Otherwise Noted)*

*The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

PERFORMANCE DATA @ $25^{\circ} \mathrm{C}$ (Unless Otherwise Noted)*

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## Manufacturing Information

## Moisture Sensitivity

(8)
All plastic encapsulated semiconductor packages are susceptible to moisture ingression. IXYS Integrated Circuits Division classified all of its plastic encapsulated devices for moisture sensitivity according to the latest version of the joint industry standard, IPC/JEDEC J-STD-020, in force at the time of product evaluation. We test all of our products to the maximum conditions set forth in the standard, and guarantee proper operation of our devices when handled according to the limitations and information in that standard as well as to any limitations set forth in the information or standards referenced below.

Failure to adhere to the warnings or limitations as established by the listed specifications could result in reduced product performance, reduction of operable life, and/or reduction of overall reliability.

This product carries a Moisture Sensitivity Level (MSL) rating as shown below, and should be handled according to the requirements of the latest version of the joint industry standard IPC/JEDEC J-STD-033.

| Device | Moisture Sensitivity Level (MSL) Rating |
| :---: | :---: |
| CPC1004N | MSL 3 |

## ESD Sensitivity

This product is ESD Sensitive, and should be handled according to the industry standard JESD-625.

## Soldering Profile

This product has a maximum body temperature and time rating as shown below. All other guidelines of J-STD-020 must be observed.

| Device | Maximum Temperature $x$ Time | Maximum Refilow Cycles |
| :---: | :---: | :---: |
| CPC1004N | $260^{\circ} \mathrm{C}$ for 30 seconds | 3 |

## Board Wash

IXYS Integrated Circuits Division recommends the use of no-clean flux formulations. However, board washing to remove flux residue is acceptable. Since IXYS Integrated Circuits Division employs the use of silicone coating as an optical waveguide in many of its optically isolated products, the use of a short drying bake could be necessary if a wash is used after solder reflow processes. Chlorine- or Fluorine-based solvents or fluxes should not be used. Cleaning methods that employ ultrasonic energy should not be used.
e3

## MECHANICAL DIMENSIONS

## CPC1004N



## CPC1004NTR Tape \& Reel



NOTE: All dimensional tolerances per Standard EIA-481-2 except as noted

## For additional information please visit our website at: www.ixysic.com

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[^0]:    $1^{1}$ Load current derates linearly from 300 mA @ $25^{\circ} \mathrm{C}$ to $100 \mathrm{~mA} @ 110^{\circ} \mathrm{C}$.
    2 Measurement taken within 1 second of on-time.

